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*Patent*  
*11/19/04*

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

**TSENG, KENNETH**

Serial No.: 10/078,732

Filing Date: February 19, 2002

For: **VARIABLE STAGE RATIO BUFFER  
INSERTION FOR NOISE OPTIMIZATION  
IN A LOGIC CIRCUIT**

Examiner: Tat, Binh C.

Art Unit: 2825

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OCT 01 2004

OFFICE OF PETITIONS

**Amendment and Response  
(37 CFR § 1.121)**

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed November 21, 2003 please amend the above-referenced application as follows and consider the following remarks: